GRACE

SPECIFICATION

ROHS Compliant Parts

Customer :

Part Name : Chip Audio Bead

Part Number : KRFB-AF Size

Dongguan GRACE electronic Technology Co., LTD

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Multilayer Chip Ferrite Audio Beads —KRFB-AF series

For Audio Lines

- High Frequency Noise



Features

- Perfect effect for EMi suppression at high frequency.
- Multilayer Chip Ferrite Beads with nickel barrier termination (AgNiSn)
- Operating temperature from -55 °C to 125°C
- Suitable reflow and wave soldering.
- 100% Pb free, RoHS

Applications

■ EMI Suppression of audio line, such as Mobile phones, TV and other equipment, etc.

Explanation of Part Numbers

KRFB	0402	AF	101	D	1R1	Α	K000	Т	
1	2	3	4	(5)	6	7	8	9	

1	Series				
	GRACE				
Multilayer Chip Ferrite					
Beads					

2	Chip size (EIA)		
	0402、0603		

3	Series code
AF	Audio Filter for High Frequency Noise

4	Nominal Impedance(Ω)	
101	100	
331	330	
462	4600	

⑤	Material Code		
D, E			

6	Rated Curent(A)
R65	0.65
1R1	1.10

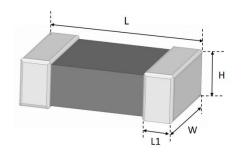
7	Internal code
	A

8	Customer identification code
	K000

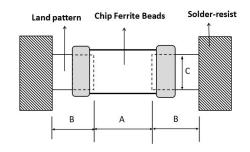
9	Packaging style
т	Таре
В	Bulk

Shape and Dimensions

1) Dimensions:



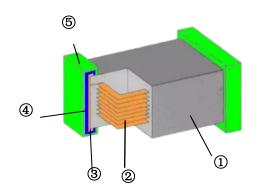
2) Recommended PCB pattern for reflow soldering:



Unit: mm

Size (EIA/JIS)	L	W	Н	L1	A	В	C
0402/1005	1.00±0.05	0.50±0.05	0.50±0.05	0.30±0.10	0.45~0.55	0.40~0.50	0.45~0.55
0603/1608	1.60 ± 0.20	0.80 ± 0.20	0.80 ± 0.20	0.30 ± 0.20	0.60~0.80	0.60~0.80	0.60~0.80

Structure and Materials



No.	Name				
1	Body				
2	Internal electrode				
3		Ag			
4	Terminal electrode	Ni			
5		Sn			

Electrical Characteristics

0402 Type

D. d.N. o.k.		Impedance (Ω)		RDC	Rated current
Part Number	@900MHz(Typ.)	@900MHz(Min.)	@1.7GHz(Typ.)	(Q) max	(mA) Max
KRFB0402AF101D1R1AK000T	100	70	160	0.100	1100
KRFB0402AF251D1R2AK000T	250	150	320	0.150	1200
KRFB0402AF331DR65AK000T	330	230	54	0.300	650
KRFB0402AF461DR90AK000T	460	300	600	0.170	900
KRFB0402AF771DR50AK000T	770	530	900	0.500	500
KRFB0402AF152DR40AK000T	1500	1000	1000	0.600	400
KRFB0402AF262DR35AK000T	2600	1800	1450	0.800	350
KRFB0402AF352DR27AK000T	3500	2500	1600	1.350	270
KRFB0402AF462DR27AK000T	4600	2800	1800	1.650	270

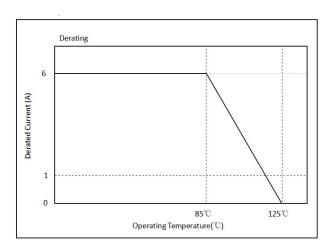
0603 Type

	Dant Manch on		Impedance (Ω)	RDC	Rated current	
	Part Number	@900MHz(Typ.)	@900MHz(Min.)	@1.7GHz(Typ.)	(Ω) max	(mA) Max
KR	RFB0603AF471E1R6AK000T	470	280	270	0.075	1600

X Notes:

- a. Standard testing conditions: Ordinary Temp.(15°C to 35°C);Ordinary Humidity (45% to 85%);
 Air pressure (86kPa to 106kPa).
- b. The DC Resistance was tested with High Accuracy Milliohmmeter-RM3545 or the equivalent.
- c. The Impedance (Z) was tested at 100MHz of High Accuracy RF Impedance /Material Analyzer-E4991 or the equivalent.
- d. Rated Current: Applied the current to coils, the temperature rise shall not be more than 20°C.

X For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 85℃, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



Reliability Test

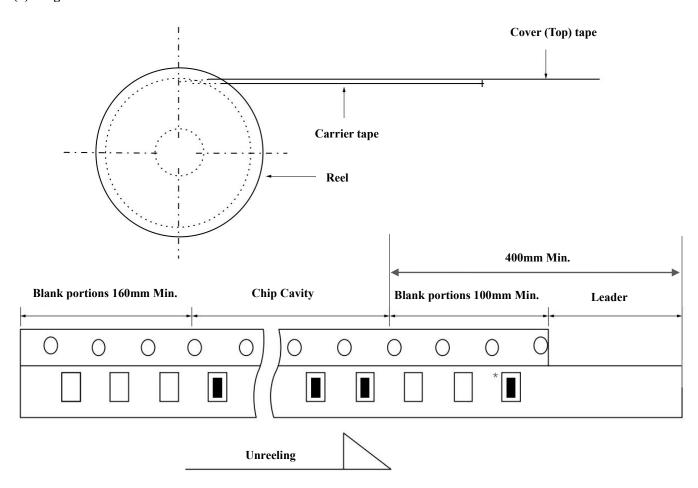
Items	Requirements	Test Methods and Remarks		
	No removal or split of the termination or other defects shall occur.	Solder the chip to the testing jig (glass epoxy board shown in the following Fig. 1-1) using eutectic solder. Then apply a force in the direction of the arrow.		
Terminal Strength	Chip Mounting Pad Glass Epoxy Board Fig.1-1	Size (EIA) 0402、0603	Force 5N	Duration 10±1s

GICACE	Spec	meane	1115 101	mumay	er chip Audio	Deaus		1 age 4 01 1-
	No visible mechanical damage. Solder the chip to the test jig (glass epoxy board shown in							
		Unit: m	m		1	Fig.2-1) using a eutectic solder. Then apply a force in the		
	Size (EIA) 0402 0603	a 0.4 1.0	b 1.5 3.0	0.5 1.2	Size (EIA)	Flexure	Pressurizing Speed	Duration
Resistance to		. h	. ф	4.5	0402、0603	2mm	<0.5mm/s	10±1s
Flexure Unit: mm			c a s o o o o o o o o o o o o o o o o o o	04	45[1.772		5 5 5 5 1.772]	Flexure
Vibration	Cu pad Solder mask Glass Epoxy Board Fig. 3-1			 Solder the chip to the testing jig (glass epoxy board show in Fig.3-1) using eutectic solder. The chip shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. The frequency ranging from 10 to 55 Hz and returning to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours). 				
Dropping	No visible me		_	⁹ /o.	Drop chip bead 10 times on a concrete flom a height of 100 cm.			
Temperature Characteristic	No visible meImpedance convalue measure	hange: w	ithin ±20°	% of initial	* Temperature * Reference te	_		
Solderability	 No visible mechanical damage. Wetting shall exceed 75% coverage for 0201 series; exceed 95% for others. 			 ❖ Solder temperature: 240±2℃. ❖ Duration: 3 sec. ❖ Solder: Sn/3.0Ag/0.5Cu. ❖ Flux: 25% Resin and 75% ethanol in weight. 			ht.	
Resistance to Soldering Heat	 No visible mechanical damage. Wetting shall exceed 75% coverage for 0201 series; exceed 95% for others. Impedance change: within ±20%. 			* The chip sha	sec. .0Ag/0.5Cu. tesin and 75%	ethanol in weig I at normal cond		

	Specifications for muturaye	
Thermal Shock	No visible mechanical damage. ❖ Impedance change: within ±20%. 125℃ 30 min. Ambient Temperature -55℃ 30 min. 20sec. (max.)	 Temperature, Time: -55°C for 30±3 min→ 125°C for 30±3min. Transforming interval: 20sec. Max. Tested cycle: 100 cycles. The chip shall be stabilized at normal condition for 1~2 hours before measuring.
Resistance to Low Temperature	 ❖ No visible mechanical damage. ❖ Impedance change: within ±20%. 	 ❖ Temperature: -55±2℃ ❖ Duration: 1000+24 hours. ❖ The chip shall be stabilized at normal condition for 1~2 hours before measuring.
Resistance to High Temperature	 ❖ No visible mechanical damage. ❖ Impedance change: within ±20%. 	 Temperature: 125±2°C Duration: 1000+24 hours. The chip shall be stabilized at normal condition for 1~2 hours before measuring.
Damp Heat (Steady States)	 ❖ No visible mechanical damage. ❖ Impedance change: within ±20%. 	 Temperature:60±2℃ Humidity: 90% to 95% RH. Duration: 1000+24 hours. The chip shall be stabilized at normal condition for 1~2 hours before measuring.
Loading Under Damp Heat	 ❖ No visible mechanical damage. ❖ Impedance change: within ±20%. 	 Temperature:60±2℃ Humidity: 90% to 95% RH. Duration: 1000+24 hours. Applied current: Max. Permissive Operating Current. The chip shall be stabilized at normal condition for 1~2 hours before measuring.
Loading at High Temperature (Life Test)	 ❖ No visible mechanical damage. ❖ Impedance change: within ±20%. 	 ❖ Temperature: 125±2℃ ❖ Duration: 1000+24 hours. ❖ Applied current: Max. Permissive Operating Current. ❖ The chip shall be stabilized at normal condition for 1~2 hours before measuring.

Packaging

(1) Figure

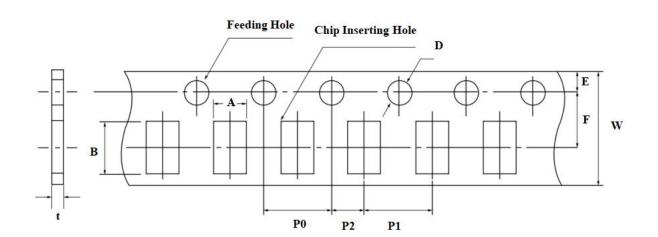


(2) Quantity

Size(EIA)		0402	0603
Taping Type		Paper	Paper
	Reel	10K	4K
Quantity	Inner Box	10K×10=100K	4K×10=40K
	Outer Box	10K×10×6=600K	4K×10×6=240K

(3) Tape Size

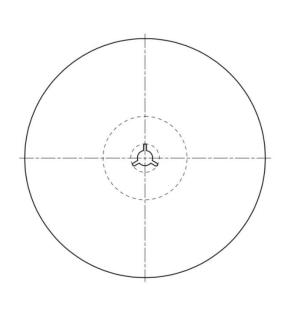
Cardboard(Paper) tape

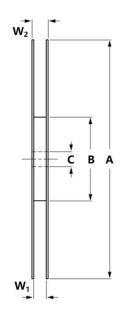


-		
	mit.	mm

Size (EIA)	A	В	W	F	E	P1	P2	P0	D	t
0402	0.65±0.1	1.15±0.1	8.00	3.50	1.75	2.00 ±0.05	2.00	4.00	ф 1.50	≤0.8
0603	1.0±0.2	1.8±0.2	±0.30	±0.05	±0.10	4.00 ±0.10	±0.05	±0.10	+0.1/-0.03	≤1.1

(4) Reel Size



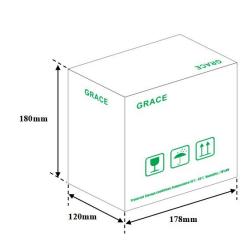


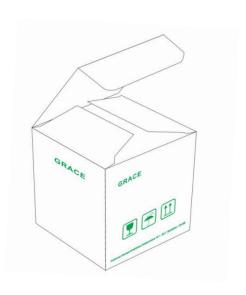
Туре	Symbol	Dimensions(mm)	
7" Reel	A	178±2	
	В	58±2	
	C	13.5±0.2	
	W1	8.4+1.5/-0.0	
	W2	≤14.4	

(5) BOX package

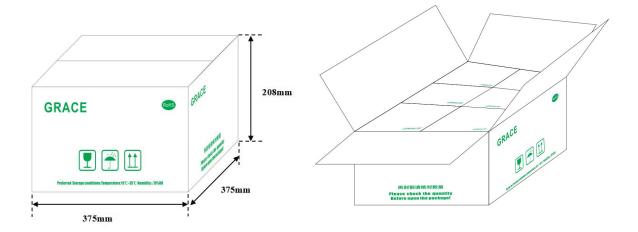
Double packaging with the paper type of inner box and outer box.

Inner Box :





Outer Box :



***** Box size specifications for reference.

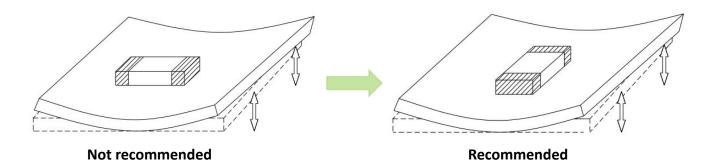
Storage environment

- (1) Recommendation for temperature/humidity
- ❖ Even taping and packaging materials are designed to endure a long-term storage, they should be stored with a temperature of -10~40 ℃ and an RH of 0~70% otherwise, too high temperatures or humidity may deteriorate the quality of the chip rapidly.
- Packaging material may be deform-ed if package are stored where they are exposed to heat of direct sunlight.
- As oxidization is accelerated when relative humidity is above 70%RH, the lower the humidity is, the better the solderability is.
- As the temperature difference may cause dew condensation during the storage of the chip, it is a must to maintain a temperature control environment.
- (2) Shelf Life
- An allowable storage period should be within 12 months from the outgoing date of delivery in consideration of solderability.
- **As for chips in storage over 12 months, please check solderability before use.**
- (3) Caution for corrosive environment

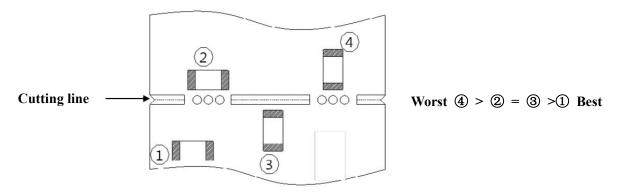
As corrosive gases may deteriorate the solderability of chip outer termination, it is a must to store chip in an environment without gases, chip that is exposed to corrosive gases may cause its quality issues due to the corrosion of plating layers and the penetration of moisture.

Process of Mounting and Soldering

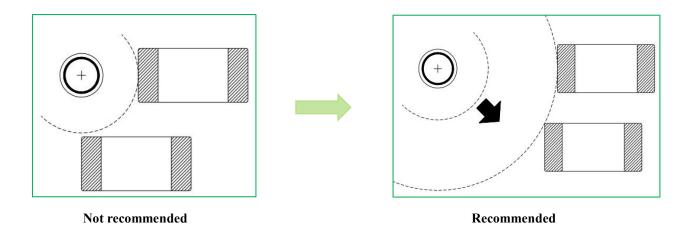
- (1) Mounting
- It is recommended to locate the major axis of chip in parallel to the direction in which the stress is applied.



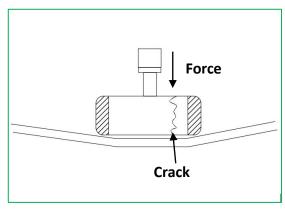
Please take the following measures to effectively reduce the stress generated from the cutting of PCB. Select the mounting location shown below, since the mechanical stress is affected by a location and a direction of chip mounted near the cutting line.



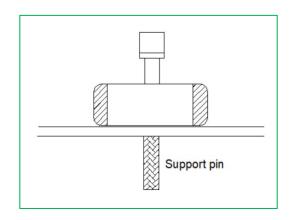
If the chip is mounted near a screw hole, the board deflection may be occurred by screw torque. Mount the chip as far from the screw holes as possible.



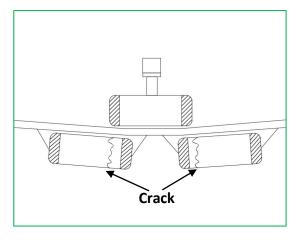
Substrate fixes up back surface of substrate with support pin in impact of suction nozzle to wely deflection to the utmost, and substrate hold deflection, please. A representative example is shown in the following.



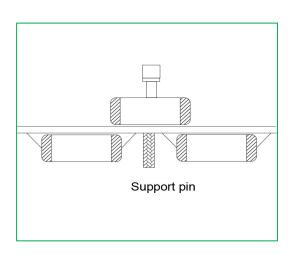
Cases to avoid



Recommended Case



Cases to avoid

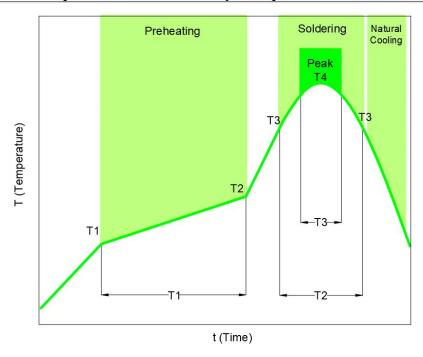


Recommended Case

Dust accumulated in a suction nozzle and suction mechanism can impede a smooth movement of the nozzle.
This may cause cracks in the chip due to the excessive force during mounting. If the mounting claw is worn out, it may cause cracks in the chip due to the uneven force during positioning. A regular inspection such as maintenance, monitor and replacement for the suction nozzle and mounting claw should be conducted.

(2) Reflow soldering

The reflow soldering temperature conditions are composed of temperature curves of Preheating, Temp. rise, Heating, Peak and Gradual cooling. Large temperature difference inside the chip caused by rapid heat application to the chip may lead to excessive thermal stresses, contributing to the thermal cracks. The Preheating temperature requires controlling with great care so that tombstone phenomenon may be prevented. Follow the recommended soldering conditions to avoid degradation of performance.



	Specification			
Item	For eutectic mixture solder	For lead-free solder		
Preheating temperature	160 ∼ 180 °C	150 ∼ 180 °C		
Solder melting temperature	200 ℃	230 ℃		
Maximum temperature	240° C max.	260 °C max.		
Preheating time	100s max.	120s max.		
Time to reach higher than the solder melting temperature	30s max.	40s max.		
number of possible reflow cycles	2 max.	2 max.		

- * Pre-heating is necessary for all constituents including the PCB to prevent the mechanical damages on the chip.
 The temperature difference between the PCB and the component surface must be kept to the minimum.
 - a. Allowable temperature difference $\triangle T \le 150$ °C
 - b. Use non-activated flux. (Max. Cl content less than 0.1%)

(3) Soldering Iron

Manual soldering can pose a great risk on creating thermal cracks in the chip. The high temperature soldering iron tip may come into a direct contact with the ceramic body of the chip due to the carelessness of an operator. Therefore, the soldering iron must be handled carefully, and close attention must be paid to the selection of the soldering iron tip and to temperature control of the tip.

Iron soldering power	Soldering time	Soldering Temp.	Number of times	Pre-heating
2011/	2	200+100€	Within each terminal	① ΔT≤130
20W max.	3s max.	300±10°C max.	once(Within total of twice)	② ≥60S

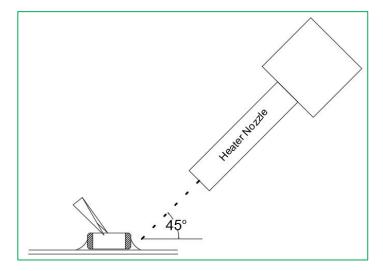
* Keep the contact time between the outer termination of the chip and the soldering iron as short as possible.
Long soldering time may cause problems such as adhesion deterioration by the leaching phenomenon of the

outer termination.

- a. Control Δ T in the solder iron and preheating temperature;
- b. Caution Iron tip should not contact with ceramic body directly;
- c. Do not cool down the chip and PCB rapidly after soldering;
- d. Lead-free solder: Sn-3.0Ag-0.5CU.

(4) Spot heater

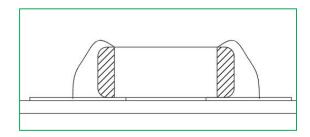
Compared to local heating with a soldering iron, hot air heating by a spot heater heats the overall component and board, therefore, it tends to lessen the thermal shock. In the case of a high density mounted board, a spot heater can also prevent concerns of the soldering iron making direct contact with the component.



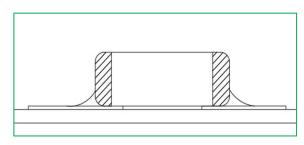
Distance	Hot Air Application angle	Hot Air Temperature Nozzle	Application Time
		Outlet	
≥ 5mm	45°C	≤ 400°C	≤ 10s

X If the distance from the hot air outlet of the spot heater to the component is too close, cracks may occur due to thermal shock. To prevent this problem, Follow the conditions set in the table above to prevent this problem.

(5) Recommended Amount of Solder







Insufficient amount

X Notes:

- a. Too much solder amount will increase the risk of PCB bending or cause other damages.
- b. Too little solder amount will result in the chip breaking loose from the PCB due to the inadequate adhesive strength.
- c. Check if the solder has been applied properly and ensure the solder fillet has a proper shape.

(6) Cleaning

In general, cleaning is unnecessary if rosin flux is used.

When acidic flux is used strongly, chlorine in the flux may dissolve into some types of cleaning fluids, thereby affecting the performance of the chip.

This means that the cleansing solution must be carefully selected and should always be new.

Cautions for cleaning

- a. Soldering flux residue may remain on the PC board if cleaned with an inappropriate solvent. This may deteriorate the performance of Varistors, especially insulation resistance.
- b. The chip or solder joint may be cracked with the vibration of PCB, if ultrasonic vibration is too strong during cleaning. Therefore, test should be done for the cleaning equipment and its process before the cleaning in order to avoid damages on the chip, you can refer to the following conditions for cleaning

Ultrasound output	Ultrasound frequency	Cleaning time
20W/liter or less	40kHz or less	5minutes or less



Limitation

Please contact us with usage environment information such as voltage, current, temperature, or other special conditions before using our products for the applications listed below. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below.

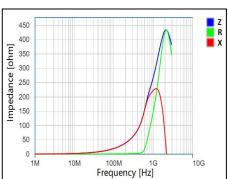
If you have any questions regarding this 'Limitation', you should first contact our sales personnel or application engineers.

- Aerospace/Aviation equipment1wheeler, 2wheeler and 3wheeler vehicle
- **Automotive of Transportation equipment**
- * Military equipment
- Atomic energy-related equipment
- Undersea equipment
- **Medical equipment**
- Disaster prevention/crime prevention equipment
- Power plant control equipment
- Traffic signal equipment
- Data-processing equipment
- Electric heating apparatus, burning equipment
- **Safety equipment**
- Any other applications with the same as or similar complexity or reliability to the applications

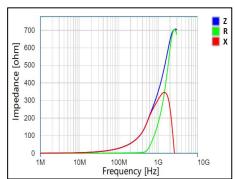
Typical Characteristic Curve

KRFB0402AF331DR65AK000T

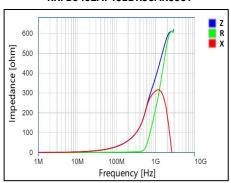
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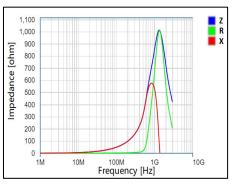
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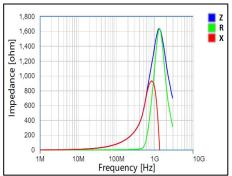
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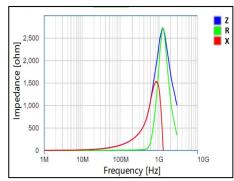
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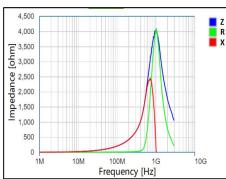
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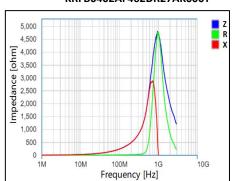
KRFB0402AF262DR35AK000T



KRFB0402AF352DR27AK000T



KRFB0402AF462DR27AK000T



KRFB0603AF471E1R6AK000T

